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JUN 30 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant Suresh Balasuramanian, et al. Docket Number: TI-36690
 Serial No.: 10/717,551 Art Unit: 2815
 Filed: 11/21/03 Examiner: Eugene Lee
 For: High Density Memory Array

CERTIFICATION OF FACSIMILE TRANSMISSION

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NAME OF INVENTOR(S): Suresh Balasuramanian, et al.	
TITLE OF INVENTION: High Density Memory Array	
TI FILE NO.: TI-36690	DEPOSIT ACCT. NO.: 20-0668
FAXED: 6-30-05 DUE: 7-13-05 ATTY/SECY: AKS/kjv	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/717,551 Filing Date: 11/21/03	

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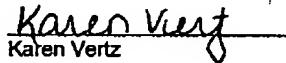
JUN 30 2005

Applicant: Suresh Balasuramanian, et al.**Art Unit:** 2815**Serial No.:** 10/717,551**Examiner:** Eugene Lee**Filed:** 11/21/03**Docket:** TI-36690**For:** High Density Memory Array**CERTIFICATION OF FACSIMILE TRANSMISSION**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:


Karen Vertz6-30-05
Date**ELECTION**

This election is offered in response to the Examiner's restriction requirement mailed June 13, 2005.

Applicant hereby elects to pursue Group I, Species I: Figure 4, claims 1-9, 15, 16-25, 31-42, 48 and 49, without traversing the Examiner's restriction requirement.

Respectfully submitted,

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